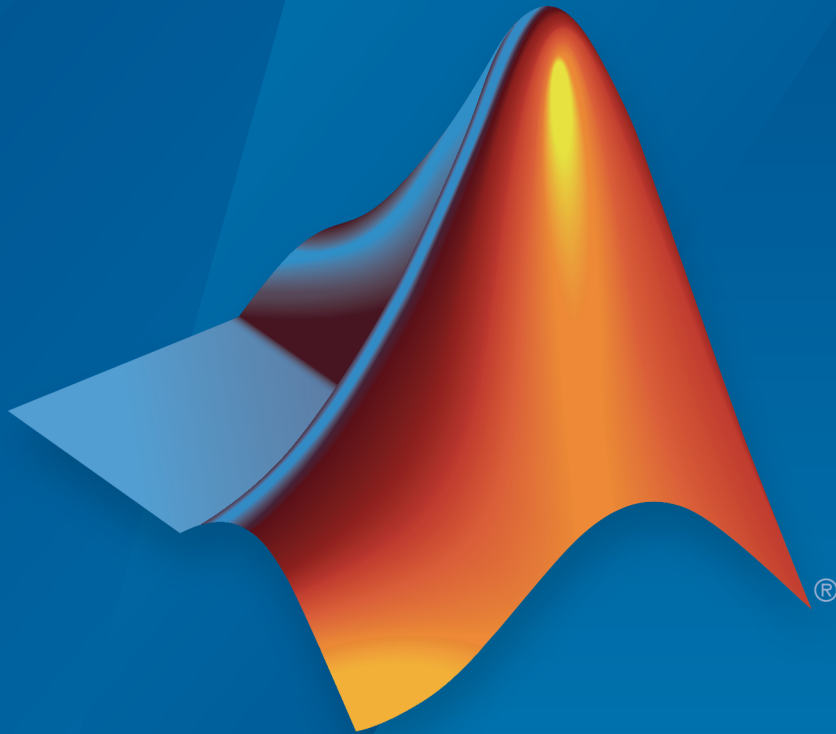


Simulink<sup>®</sup> Test<sup>™</sup>  
Getting Started Guide



MATLAB<sup>®</sup>&SIMULINK<sup>®</sup>

R2016b



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### *Simulink<sup>®</sup> Test<sup>™</sup> Getting Started Guide*

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## Product Overview

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# Product Overview

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## Simulink Test Product Description

### **Develop, manage, and execute simulation-based tests**

Simulink® Test™ provides tools for authoring, managing, and executing systematic, simulation-based tests of models, generated code, and simulated or physical hardware. It includes a Test Sequence block that lets you construct complex test sequences and assessments, and a Test Manager for managing and executing tests. Simulink Test enables functional, baseline, equivalence, and back-to-back testing, including software-in-the-loop (SIL), processor-in-the-loop (PIL), and real-time hardware-in-the-loop (HIL). You can apply pass and fail criteria that include absolute and relative tolerances, limits, logical checks, and temporal conditions. Setup and cleanup scripts help you automate or customize test execution.

You can create nonintrusive test harnesses to test components in the system model or in a separate test model. You can store test cases and their results, creating a repository for reviewing and investigating failures. You can generate reports, archive and review test results, rerun failed tests, and debug the component or system under test.

With Simulink Test and Simulink Verification and Validation™, you can link test cases to requirements captured in Microsoft® Word, IBM® Rational® DOORS®, and other documents.

Support for industry standards is available through IEC Certification Kit (for IEC 61508 and ISO 26262) and DO Qualification Kit (for DO-178).

### **Key Features**

- Test harness for subsystem or model testing
- Test sequence block for running tests and assessments
- Pass-fail criteria, including tolerances, limits, and temporal conditions
- Baseline, equivalence, back-to-back, and real-time testing
- Setup and cleanup scripts for customizing test execution
- Test Manager for authoring, executing, and organizing test cases and their results
- Customizable report generation for documenting test outcomes

# Introduction

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## Refine, Test, and Debug a Subsystem

In this section...
“Model and Requirements” on page 2-2
“Create a Harness for the Controller” on page 2-4
“Inspect and Refine the Controller” on page 2-6
“Add a Test Case and Test the Controller” on page 2-6
“Debug the Controller” on page 2-7

Test harnesses provide a development and testing environment that leaves the main model design intact. You can test a functional unit of your model in isolation without altering the main model. This example demonstrates refining and testing a controller subsystem using a test harness. The main model is a controller-plant model of an air conditioning/heat pump unit. The controller must operate according to several simple requirements.

### Model and Requirements

- 1 Access the model. Enter

```
cd(fullfile(docroot, 'toolbox', 'sltest', 'examples'))
```

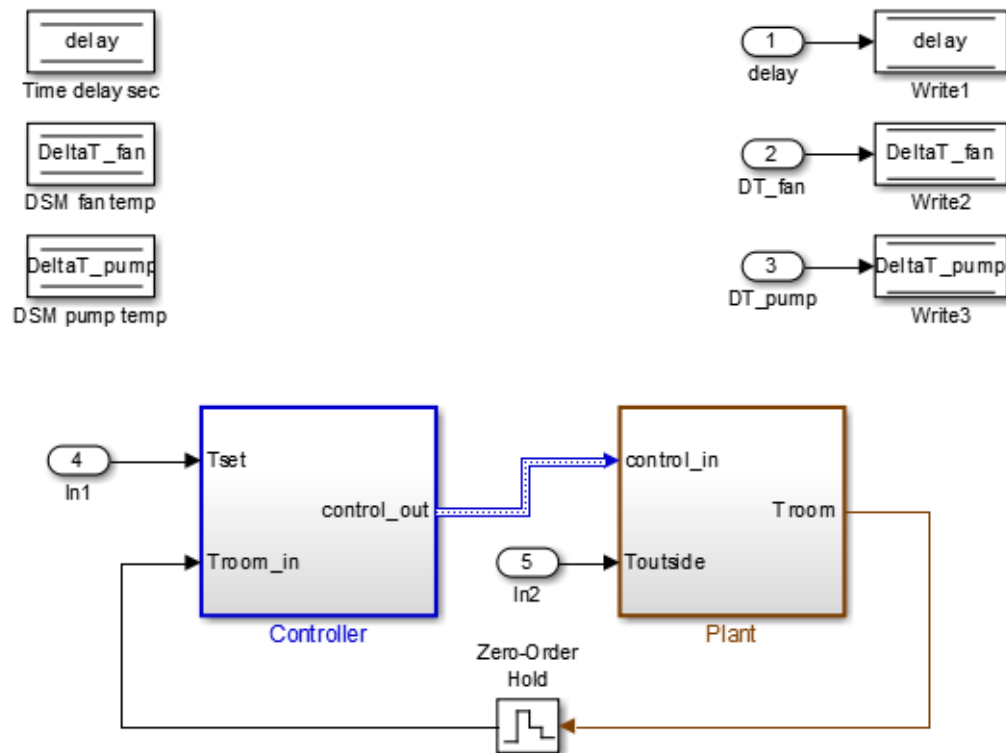
- 2 Copy this model file and supporting files to a writable location on the MATLAB<sup>®</sup> path:

```
sltestHeatpumpExample.slx  
sltestHeatpumpBusPostLoadFcn.mat  
PumpDirection.m
```

- 3 Open the model.

```
open_system('sltestHeatpumpExample')
```





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In the example model:

- The controller accepts the room temperature and the set temperature inputs.
- The controller output is a bus with signals controlling the fan, heat pump, and the direction of the heat pump (heat or cool).
- The plant accepts the control bus. The heat pump and the fan signals are Boolean, and the heat pump direction is specified by +1 for cooling and -1 for heating.

The test covers four temperature conditions. Each condition corresponds to one operating state with fan, pump, and pump direction signal outputs.

Temperature condition	System state	Fan command	Pump command	Pump direction
$ T_{room} - T_{set}  < \Delta T_{fan}$	idle	0	0	0
$\Delta T_{fan} \leq  T_{room} - T_{set}  < \Delta T_{pump}$	fan only	1	0	0
$ T_{room} - T_{set}  \geq \Delta T_{pump}$ and $T_{set} < T_{room}$	cooling	1	1	-1
$ T_{room} - T_{set}  \geq \Delta T_{pump}$ and $T_{set} > T_{room}$	heating	1	1	1

## Create a Harness for the Controller

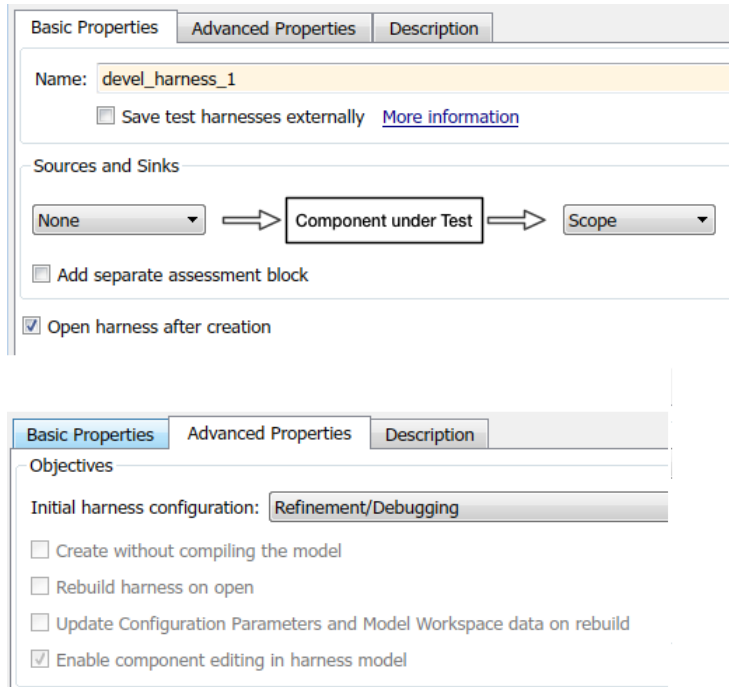
- 1 Right-click the Controller subsystem and select **Test Harness > Create for 'Controller'**.
- 2 Set the harness properties:

In the **Basic Properties** tab:

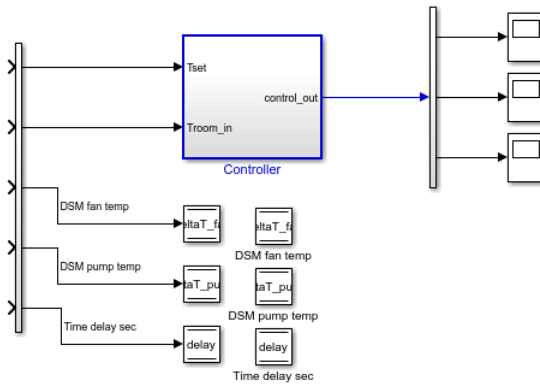
- **Name:** devel\_harness\_1
- **Sources and Sinks:** None and **Scope**
- Clear **Add separate assessment block**
- Select **Open harness after creation**.

In the **Advanced Properties** tab:

- **Initial harness configuration:** Refinement/Debugging

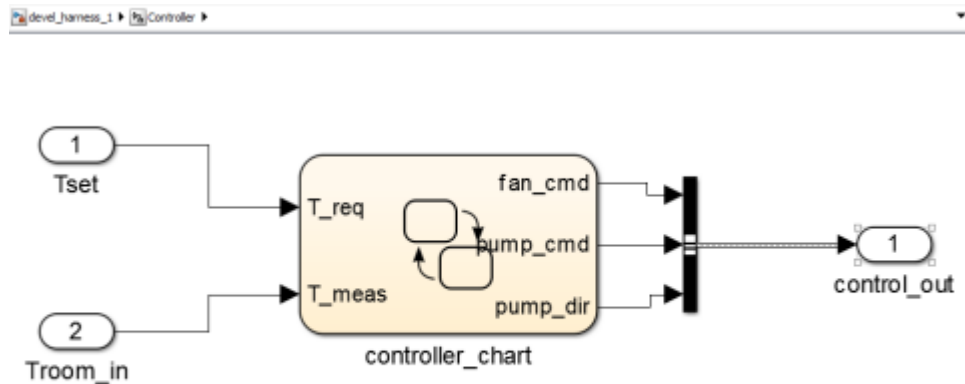


3 Click **OK** to create the test harness.



### Inspect and Refine the Controller

- 1 In the test harness, double-click **Controller** to open the subsystem.
- 2 Connect the chart to the inports.

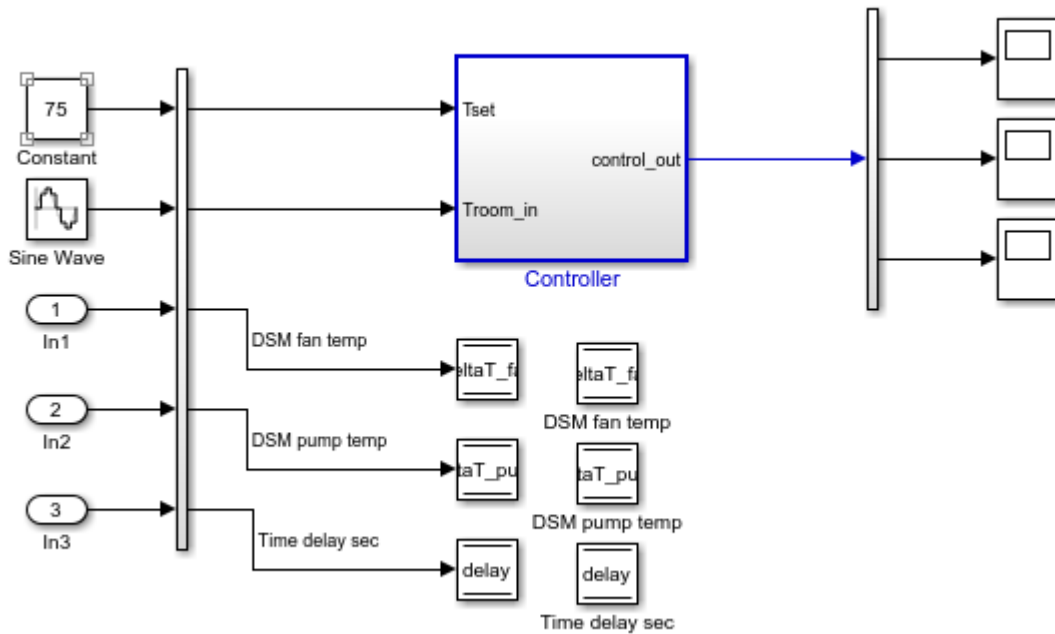


- 3 In the test harness, click save to save the test harness and model.

### Add a Test Case and Test the Controller

- 1 Navigate to the top level of `devel_harness_1`.
- 2 Create a test input for the harness with a constant **Tset** and a time-varying **Troom**. Connect a Constant block to the **Tset** input and set the value to 75.
- 3 Add a Sine Wave block to the harness model to simulate a temperature signal. Connect the Sine Wave block to the conversion subsystem input **Troom\_in**.
- 4 Double-click the Sine Wave block and set the parameters:
  - **Amplitude:** 15
  - **Bias:** 75
  - **Frequency:**  $2 \cdot \pi / 3600$
  - **Phase (rad):** 0
  - **Sample time:** 1

- Select **Interpret vector parameters as 1-D**.
- 5 Connect Inport blocks to the Data Store Write inputs.

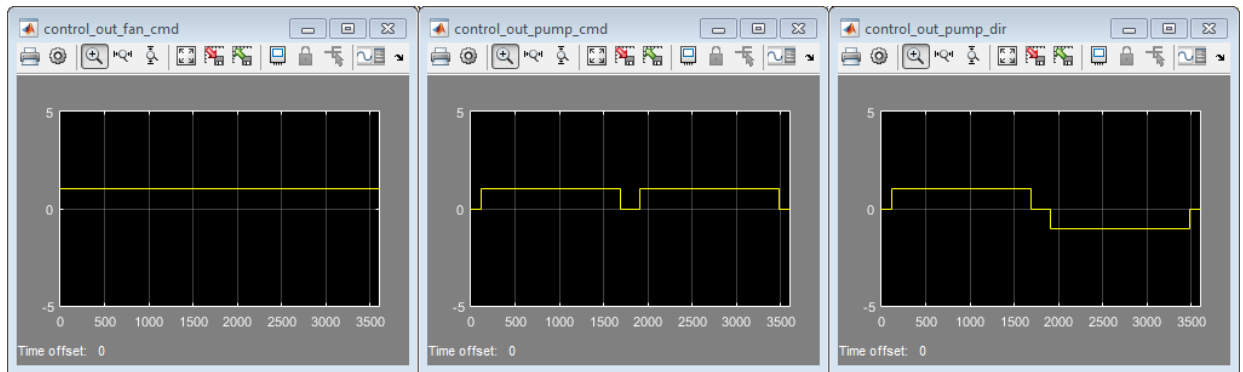


- 6 In the Configuration Parameters dialog box, in the **Data Import/Export** pane, select **Input** and enter `u`. `u` is an existing structure in the MATLAB base workspace.
- 7 In the **Solver** pane, set **Stop time** to 3600.
- 8 Open the three scopes in the harness model.
- 9 Simulate the harness.

## Debug the Controller

- 1 Observe the controller output. `fan_cmd` is 1 during the IDLE condition where  $|T_{room} - T_{set}| < \Delta T_{fan}$ .

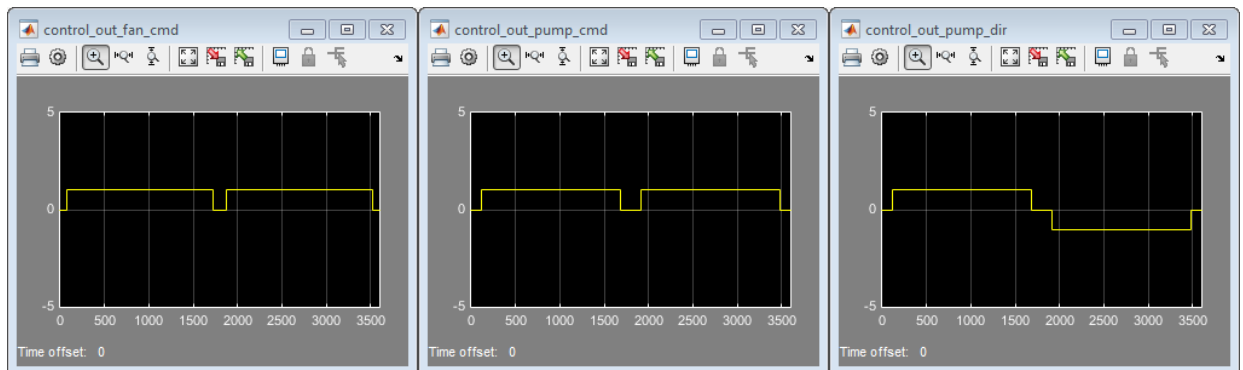
This is a bug. `fan_cmd` should equal 0 at IDLE. The `fan_cmd` control output must be changed for IDLE.



- 2 In the harness model, open the **Controller** subsystem.
- 3 Open `controller_chart`.
- 4 In the IDLE state, `fan_cmd` is set to return 1. Change `fan_cmd` to return 0. IDLE is now:

```
IDLE
entry:
fan_cmd = 0;
pump_cmd = 0;
pump_dir = 0;
```

- 5 Simulate the harness model again and observe the outputs.



- 6 `fan_cmd` now meets the requirement to equal 0 at IDLE.

## **Related Examples**

- “Test a Model Component Using Signal Functions”
- “Test Downshift Points of a Transmission Controller”


# Test Model Output Against a Baseline

To test the simulation output of a model against a defined baseline data set, use a baseline test case. In this example, use the `sldemo_absbrake` model to compare the simulation output to a baseline that is captured from an earlier state of the model.

## Create the Test Case

- 1 Open the `sldemo_absbrake` model.
- 2 To open the Test Manager from the model, select **Analysis > Test Manager**.
- 3 From the Test Manager toolstrip, click **New** to create a test file. Name and save the test file.

The new test file consists of a test suite that contains one baseline test case. They appear in the **Test Browser** pane.

- 4 Right-click the baseline test case in the **Test Browser** pane, and select **Rename**. Rename the test case to **Slip Baseline Test**.
- 5 Under **System Under Test** in the test case, click the **Use current model** button  to load the `sldemo_absbrake` model into the test case.
- 6 Under the **Baseline Criteria** section, click **Capture** to record a baseline data set from the model specified under **System Under Test**.


Save the baseline data set to a location. After you save the baseline MAT-file, the model runs and the baseline criteria appear in the table.

- 7 Expand the baseline data set. Set the **Absolute Tolerance** of the first `yout` signal to 15, which corresponds to the `Ww` signal.



SIGNAL NAME	ABS TOL	REL TOL	
▼ <input checked="" type="checkbox"/> test_capture.mat	0	0.00%	+
<input checked="" type="checkbox"/> yout.Ww	15	0.00%	
<input checked="" type="checkbox"/> yout.Vs	0	0.00%	
<input checked="" type="checkbox"/> yout.Sd	0	0.00%	
<input checked="" type="checkbox"/> slp	0	0.00%	

+ Add... Capture... Refresh Delete

To add or remove columns in the baseline criteria table, click the column selector button . For more information about tolerances and criteria, see “How Tolerances Are Applied to Test Criteria”.

## Run the Test Case and View Results

- 1 In the `sldemo_absbrake` model, set the **Desired relative slip** constant block to 0.22.
- 2 In the Test Manager, select the Slip Baseline Test case in the **Test Browser** pane.
- 3 On the Test Manager toolstrip, click **Run** to run the selected test case.

The Test Manager switches to the **Results and Artifacts** pane, and the new test result appears at the top of the table.

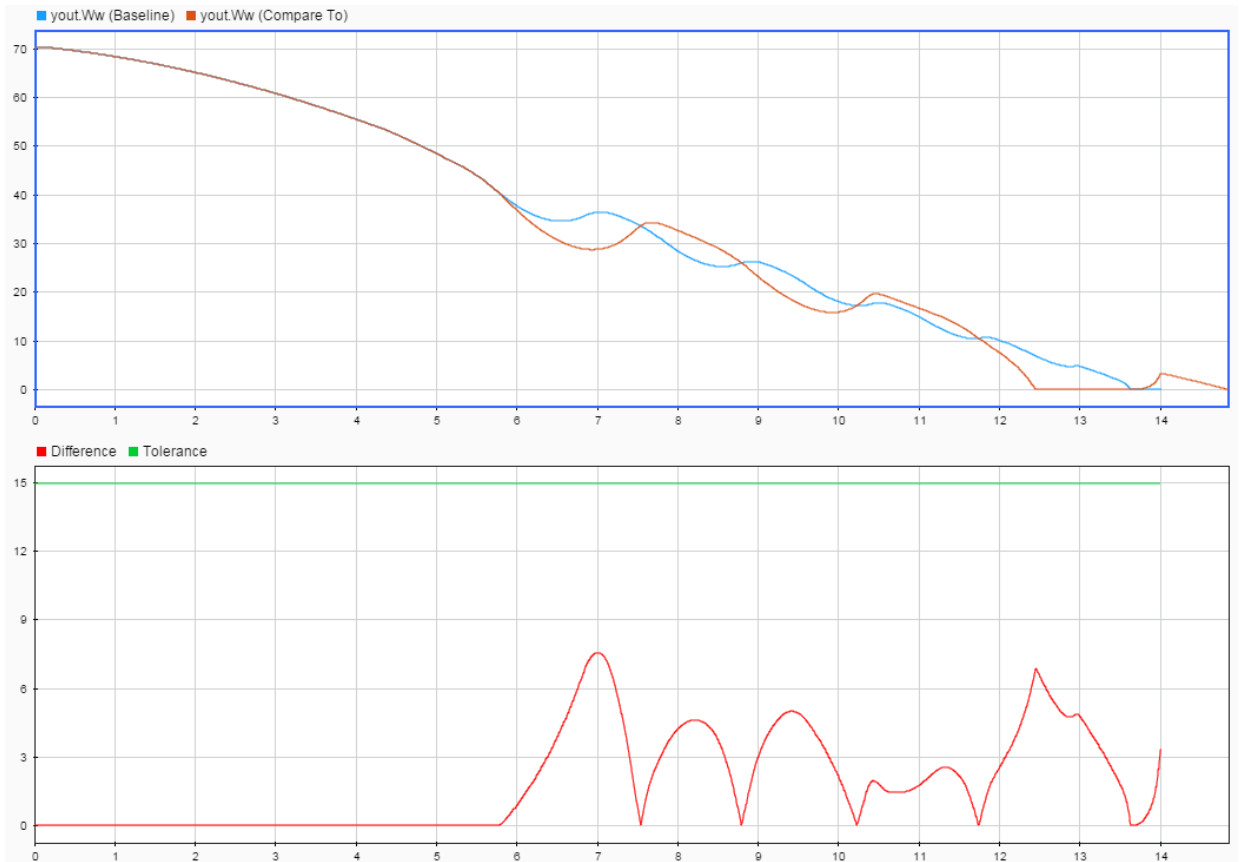
- 4 Expand the results until you see the baseline criteria result.

The signal `yout.Ww` passes, but the overall baseline test fails because other signal comparisons specified in the **Baseline Criteria** section of the test case were not satisfied.

- 5 To view the `yout.Ww` signal comparison between the model and the baseline criteria, expand **Baseline Criteria Result** and click the option button next to the `yout.Ww` signal.

Baseline Criteria Result	
<input type="radio"/> slp	✘
<input type="radio"/> yout.Sd	✘
<input type="radio"/> yout.Vs	✘
<input checked="" type="radio"/> yout.Ww	✔

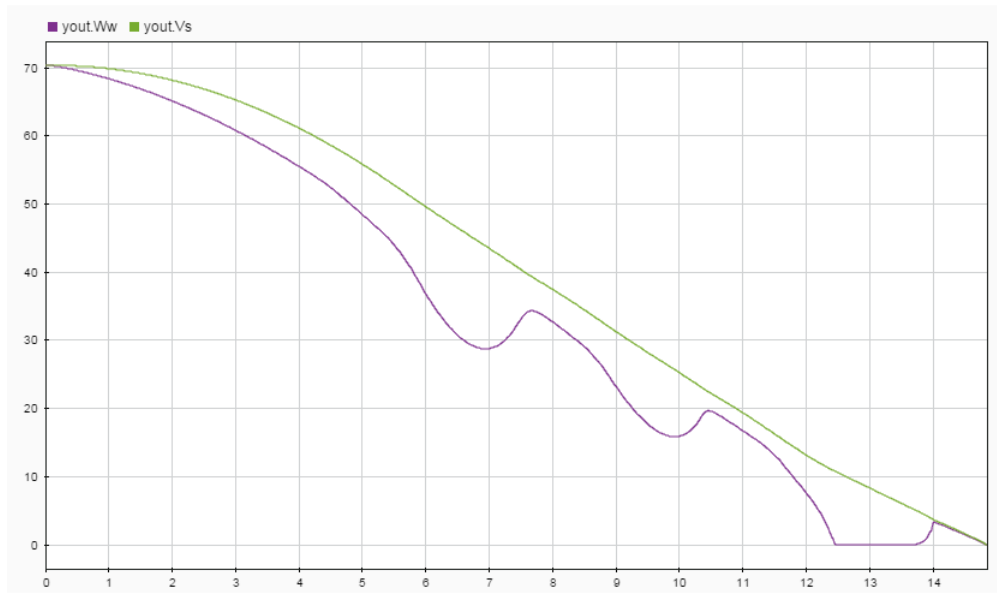
The **Comparison** tab opens and shows the criteria comparisons for the `yout.Ww` signal.



- 6 You can also view signal data from the simulation. Expand **Sim Output** and select the signals you want to plot.

Sim Output (sldemo_absbrake)		
<input type="checkbox"/>	slp	—
<input type="checkbox"/>	yout.Sd	—
<input checked="" type="checkbox"/>	yout.Vs	—
<input checked="" type="checkbox"/>	yout.Ww	—

The **Visualize** tab opens and plots the simulation output.



For information on how to export results and generate reports from results, see “Export Test Results and Generate Reports”.

# Introduction to the Test Manager

### In this section...

“Test Manager Description” on page 2-14

“Test Creation and Hierarchy” on page 2-14

“Test Results” on page 2-15

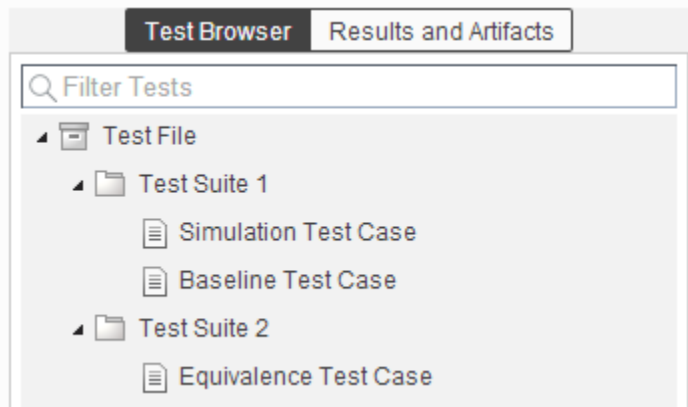
“Share Results” on page 2-15

## Test Manager Description

The Test Manager in Simulink Test enables you to automate Simulink model testing and organize large sets of tests. A model test is performed using test cases where criteria are specified to determine a pass-fail outcome. The test cases are run from the Test Manager. At the end of a test, the test case results are organized and viewed in the Test Manager.

## Test Creation and Hierarchy

Test cases are contained within a hierarchy of test files and test suites in the **Test Browser** pane of the Test Manager. A test file can contain multiple test suites, and test suites can contain multiple test cases.



There are three types of test case templates to choose from in the Test Manager. Each test case uses a different set of criteria to determine the outcome of a test.

- **Baseline:** compares signal outputs of a simulation to a baseline set of signals. The comparison of the simulation output and the baseline must be within the absolute or relative tolerances to pass the test, which is defined in the **Baseline Criteria** section of the test case.
- **Equivalence:** compares signal outputs between two simulations. The comparison of outputs must be within the absolute or relative tolerances to pass the test, which is defined in the **Equivalence Criteria** section of the test case.
- **Simulation:** checks that a simulation runs without errors, which includes model assertions.

## Test Results

Results of a test are given using a pass-fail outcome. If all of the criteria defined in a test case is satisfied, then a test passes. If any of the criteria are not satisfied, then the test fails. Once the test has finished running, the results are viewed in the **Results and Artifacts** pane. Each test result has a summary page that highlights the outcome of the test: passed, failed, or incomplete. The simulation output of a model is also shown in the results section. Signal data from the simulation output can be visually inspected using the Simulation Data Inspector.

## Share Results

Once you have completed the test execution and analyzed the results, you can share the test results with others or archive them. If you want to share the results to be viewed later in the Test Manager, then you can export the results to a file. To archive the results in a document, you can generate a report, which can include the test outcome, test summary, and any criteria used for test comparisons.

## Related Examples

- “Test Model Output Against a Baseline”
- “Test Two Simulations for Equivalence”
- “Code Generation Verification Workflow with Simulink Test”

